



## **Supplemental Application Data Sheet**

### **Application Information**

Application number::	10/714,536
Filing Date::	11/13/03
Application Type::	Regular
Subject Matter::	Utility
Suggested Group Art Unit::	N/A
CD-ROM or CD-R?::	None
Sequence submission?::	None
Computer Readable Form (CRF)?::	No
Title::	POLYMER TRANSISTOR ARRANGEMENT, INTEGRATED CIRCUIT ARRANGEMENT AND METHOD FOR PRODUCING A POLYMER TRANSISTOR ARRANGEMENT
Attorney Docket Number::	20046/0200501-US0
Request for Early Publication?::	No
Request for Non-Publication?::	No
Suggested Drawing Figure::	Fig. 1
Total Drawing Sheets::	4
Small Entity?::	No
Petition included?::	No
Secrecy Order in Parent Appl.?::	No

### **Applicant Information**

Applicant Authority Type::	Inventor
Primary Citizenship Country::	Germany
Status::	Full Capacity
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Family Name::	Brederlow
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City of mailing address:: Munich  
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### **Correspondence Information**

Correspondence Customer Number:: 07278

### **Representative Information**

Representative Customer Number:: 07278

### **Foreign Priority Information**

Country::	Application number::	FilingDate::	Priority Claimed::
Germany	102 47 817.1	10/14/02	Yes

### **Assignee Information**

Assignee name:: Infineon Technologies AG  
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Postal or Zip Code of mailing address:: 81669